ABSTRACT OF THE DISCLOSURE

A method, device, and system for sharing assets between the host processor and security hardware using a master mode. The method includes setting a master mode bit for a bus interface logic and passing a data request through the bus interface logic only for a specified device. The method also includes receiving data in response to the data request from the specified device and resetting the master mode bit. A computer system includes a master device configured to set and reset a master mode bit and one or more bus interface logics. Each of the bus interface logics is configured with a storage location configured to store the master mode bit, wherein the bus interface logics are configured to exchange data only with the master device when the master mode bit is set.